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Module Introduction - Oakland University

The HCS12 allows the program to jump to any memory address during program execution, and the jump to subroutine instruction may be used to call a routine that needs to be executed on periodic bases To reference the information in the CPU12 Users Manual, see the document CPU12RM/AD or go to the web site listed here Instruction Set •Data Handling

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C programming for embedded system applications

file" for each microcontroller, which defines memory addresses and symbolic labels for CPU and peripheral function register addresses #include "STM32L1xxh" /* target uC information */ // GPIOA configuration/data register addresses are defined in STM32L1xxh C programming for embedded system applications

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